

(b) a pair of junction electrode layers non-contiguously  
5 formed on said substrate;

*omit*  
(c) an adhesive layer disposed on said substrate, said  
adhesive layer being non-contiguous with said pair of junction  
electrode layers;

(d) a sample fixed to said adhesive layer and being non-  
10 contiguous with said pair of junction electrode layers;

(e) a pair of electrode layers formed on said sample; and

(f) two electrically-conductive wires, a first electrically-  
conductive wire connecting one of said electrode layers with one  
of said junction electrode layers, and a second electrically-  
15 conductive wire connecting the other of said electrode layers  
with the other of said junction electrode layers.

*Q2*  
4. (Amended) A sample assembly according to claim 3, wherein  
each of said pair of electrode layers and said pair of junction  
electrode layers is made of a multilayer including a top layer  
20 which is a gold layer, and said wires are gold wires.

*Q3*  
7. (Amended) A sample assembly according to claim 1, wherein  
said adhesive layer is made of gold-tin alloy.

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14. (New) A sample assembly according to claim 1, wherein  
the adhesive layer is made of a material selected from a group  
consisting of indium and gold-tin alloy.

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